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APPLICANTS Mitsutoshi Fujita, Osaka, JAPAN; Shuji Kondo, Ibaraki, JAPAN;				
** CONTINUING DATA ***** <i>CME</i>				
** FOREIGN APPLICATIONS ***** <i>CME</i> JAPAN 2003-122717 04/25/2003				
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met		STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS 11
Verified and Acknowledged Allowance <i>CME</i> Examiner's Signature <i>CME</i> Initials		INDEPENDENT CLAIMS 2		
ADDRESS McDermott, Will & Emery 600 13th Street, N.W. Washington, DC20005-3096				
TITLE Method of designing low-power semiconductor integrated circuit				
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	